



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - III EXAMINATION
HELD IN NOVEMBER 2015
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3301	P R No : 200800895	Sex : F	Name : CHANEKAR ADITI GANPAT		
No Of Attempts : 1			No Of Credits	Grade Obtained	SGPA
	System on Chip (SOC)				
		Theory	4	BB P	
		IA	2	AB P	
	TCP/IP and Implementation of a Network IP on FPGA				
		Theory	4	BC P	
		IA	2	BB P	
	Project				
		IA	4	AA P	
		Oral	8	AA P	
		Total :	24		7.92 P PASSES
Seat No : 3302	P R No : 201406597	Sex : F	Name : DESAI SHRADDHA SADAGURU		
No Of Attempts : 1			No Of Credits	Grade Obtained	SGPA
	System on Chip (SOC)				
		Theory	4	AB P	
		IA	2	AA P	
	TCP/IP and Implementation of a Network IP on FPGA				
		Theory	4	AA P	
		IA	2	AA P	
	Project				
		IA	4	AA P	
		Oral	8	AA P	
		Total :	24		8.83 P PASSES
Seat No : 3303	P R No : 200800913	Sex : F	Name : DHARGALKAR SHRUTI RANGANATH		
No Of Attempts : 1			No Of Credits	Grade Obtained	SGPA
	System on Chip (SOC)				
		Theory	4	AB P	
		IA	2	AA P	
	TCP/IP and Implementation of a Network IP on FPGA				
		Theory	4	AB P	
		IA	2	BB P	
	Project				
		IA	4	AA P	
		Oral	8	AA P	
		Total :	24		8.50 P PASSES
Seat No : 3304	P R No : 201004714	Sex : F	Name : KANADE SHRADDHA SABAJI		
No Of Attempts : 1			No Of Credits	Grade Obtained	SGPA
	System on Chip (SOC)				
		Theory	4	CC P	
		IA	2	BC P	
	TCP/IP and Implementation of a Network IP on FPGA				
		Theory	4	CC P	
		IA	2	BC P	
	Project				
		IA	4	AB P	
		Oral	8	BB P	
		Total :	24		6.33 P Result Reserve Sem I Not Passed



COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No :	P R No :	Sex :	Name :	No Of Credits	Grade Obtained	SGPA
3305	201406592	F	MADANDA NISHA MONAPPA			
No Of Attempts : 1						
Process and Device Characterization and Measurements						
				Theory 4	BB P	
				IA 2	AA P	
TCP/IP and Implementation of a Network IP on FPGA						
				Theory 4	BB P	
				IA 2	AB P	
Project						
				IA 4	AA P	
				Oral 8	AA P	
				Total : 24		8.25 P PASSES
3306	200806843	M	NAIK GAONKAR KAUSHIK RAJENDRA			
No Of Attempts : 1						
System on Chip (SOC)						
				Theory 4	AB P	
				IA 2	BB P	
TCP/IP and Implementation of a Network IP on FPGA						
				Theory 4	BB P	
				IA 2	AB P	
Project						
				IA 4	AA P	
				Oral 8	AA P	
				Total : 24		8.25 P PASSES
3307	201008515	F	NAIK SHRUNKHALA SANTOSH			
No Of Attempts : 1						
Process and Device Characterization and Measurements						
				Theory 4	BC P	
				IA 2	AA P	
TCP/IP and Implementation of a Network IP on FPGA						
				Theory 4	BB P	
				IA 2	BB P	
Project						
				IA 4	AA P	
				Oral 8	AA P	
				Total : 24		8.00 P PASSES
3308	200800872	M	NORONHA ALWIN			
No Of Attempts : 1						
Process and Device Characterization and Measurements						
				Theory 4	BC P	
				IA 2	AB P	
TCP/IP and Implementation of a Network IP on FPGA						
				Theory 4	CC P	
				IA 2	CC P	
Project						
				IA 4	BB P	
				Oral 8	BC P	
				Total : 24		6.08 P Result Reserve Sem I Not Passed



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Seat No : 3309	P R No : 200801359	Sex : F	Name : PARKER NEHA PRASAD
No Of Attempts : 1			
		No Of Credits	Grade Obtained SGPA
System on Chip (SOC)			
	Theory	4	AA P
	IA	2	AB P
TCP/IP and Implementation of a Network IP on FPGA			
	Theory	4	BB P
	IA	2	BB P
Project			
	IA	4	AB P
	Oral	8	AB P
Total :		24	7.92 P Result Reserve Sem II Not Passed
Seat No : 3310	P R No : 201008295	Sex : F	Name : PRABHU DESAI SANJANA SANTOSH
No Of Attempts : 1			
		No Of Credits	Grade Obtained SGPA
Process and Device Characterization and Measurements			
	Theory	4	AA P
	IA	2	AA P
TCP/IP and Implementation of a Network IP on FPGA			
	Theory	4	AA P
	IA	2	AA P
Project			
	IA	4	AO P
	Oral	8	AO P
Total :		24	9.50 P PASSES
Seat No : 3311	P R No : 200806910	Sex : M	Name : SATARDEKAR GIRISH GURUDAS
No Of Attempts : 1			
		No Of Credits	Grade Obtained SGPA
Process and Device Characterization and Measurements			
	Theory	4	CC P
	IA	2	AB P
TCP/IP and Implementation of a Network IP on FPGA			
	Theory	4	CC P
	IA	2	BC P
Project			
	IA	4	AB P
	Oral	8	BB P
Total :		24	6.50 P Result Reserve Sem I Not Passed
Seat No : 3312	P R No : 201008542	Sex : M	Name : SOORAJ SURESH KUMAR
No Of Attempts : 1			
		No Of Credits	Grade Obtained SGPA
Process and Device Characterization and Measurements			
	Theory	4	BC P
	IA	2	AA P
TCP/IP and Implementation of a Network IP on FPGA			
	Theory	4	BB P
	IA	2	AB P
Project			
	IA	4	AA P
	Oral	8	AA P
Total :		24	8.08 P PASSES

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COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3313 P R No : 201006303 Sex : M Name : TRAVASSO VALERIUS SIRUS
No Of Attempts : 1

	No Of Credits	Grade Obtained	SGPA
System on Chip (SOC)			
Theory	4	BB P	
IA	2	AA P	
TCP/IP and Implementation of a Network IP on FPGA			
Theory	4	BC P	
IA	2	BB P	
Project			
IA	4	AA P	
Oral	8	AB P	
Total :	24		7.67 P PASSES

Seat No : 3314 P R No : 200806127 Sex : F Name : KUDALKAR SHREYA S.
No Of Attempts : 2

	No Of Credits	Grade Obtained	SGPA
System on Chip (SOC)			
Theory	4	BC +	
IA	2	CC +	
Sensors in Instrumentation			
Theory	4	BC +	
IA	2	CC +	
Project			
IA	4	AB +	
Oral	8	ABS A	
Total :	24		4.17 F FAILS

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By : *Pphello*

Checked By : *KRL*

Date : *18/3/16*

S.S.J. Figueiredo
18/3/2016

S.S.J. Figueiredo
Assistant Registrar-E(Prof.)

Leo V. Macedo
18/3/16

Leo V. Macedo
Controller Of Examinations

Prof. V.P. Kamat
18.3.16

Prof. V.P. Kamat
Registrar

